

EDSAC Archives

COMP A.1 (1)

The EDSAC: general description

May 1948, 34 pages (quarto)

Typewritten report detailing the design of the EDSAC, written during the period of its construction. Contains the following headings.

- 1 Introduction
- 2 Representation of numbers and orders
- 3 Components
 - 3.1 Memory (ref Wilkes and Renwick in *Electronic Engineering*, 20, p208, July 1948)
 - 3.2 Input
 - 3.3 Control
 - 3.4 Computer
 - 3.5 Output

- Fig 1 Constitutions of Major Cycles
Fig 2 Systems of regular repetitive pulses
Fig 3 Main Control Unit Schematic
Fig 4(a) Half Adder
Fig 4(b) Adder

COMP A.1 (2)

The EDSAC: tactical functions of components

43 pages (all quarto except one large sheet)

- 4 Components of the EDSAC - Tactical Functions
 - 4.1 Basic elements
 - 4.2 Memory
 - 4.3 Input
 - 4.4 Control
 - 4.5 Computer
 - 4.6 Output
 - 4.7 Pulse generators and Power supplies

- Fig 5 Basic circuit of a flip flop
Fig 6 Coincidence Unit
Fig 7 Main Control Unit
Fig 8 Transfer Unit
Fig 9 Computer
Fig 10 Accumulator Shifting Unit
Fig 11 Adder
Fig 12 Complementer - Collator
Fig 13 Delays in Computer
Fig 14 Timing Control Table Shifting Unit
Fig 15 Computer Control Unit

"As planned some 3000 or more valves, including diodes requires HT supply at 250V, 12-15 amps and heater load 6KW"

COMP A.14

Notebook. This was used by M. V. Wilkes when working on his Ph.D., 1934-7, and when obtaining design data for EDSAC ultrasonic memory, 1945-7; Creator: Wilkes, M.V.; 1 volume; 1934-1947

pp 20-28 (dated 18/1/47) - Mercury tank notes

COMP A.15

Annotated EDSAC circuit diagrams

1 booklet; Jan. 1948-Mar. 1948; 24 pages (quarto)

Notes in the handwriting of Eric Mutch

COMP A.21

Photographs of EDSAC in construction

18 glass negatives and 1 print (110 x 90 mm); 1946-1950